Stephen West

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EDUCATION

Bachelor of Science in Electrical Engineering, Brigham Young University, Provo UT, April 2008

EXPERIENCE

Bluehost, technical support 4/2011 - present
- Provided customer technical support for webhosting and network troubleshooting
- Just Host Team Lead: Provided help and training to other technicians
- Level 3 Technician: Maintained server avalibility customer uptime

BYU Mars Rover Team 9/2007 - 6/2010
- Designed and built a remote operated simulation rover with multi-discipline engineering team
- Programed embedded Linux devices to interface with a variety of sensors and actuators
- Competed in three University Rover Challenges

BYU ECEn Department Research Assistant 1/2008 - 12/2009
- Programmed applications on the Ambric device (a 336 core parallel processor) to show comparisons between Ambric and existing FPGA designs

BYU EE Senior Project 1/2008 - 4/2008
- Programmed a quadroter to achieve stable flight over a target by using a camera to detect positions of LEDs
- Programed a Kalman filter based on a Linux platform that combined measurements from different processors in order to calculate the positional data of the quadroter

BYU Teaching Assistant 9/2007 - 12/2007
- Instructed students in FPGA circuit design written in VHDL for the Advanced Digital Design class

BYU Broadcasting, web development 8/2004 - 12/2007
- Maintained a dynamic corporate intranet to support HR, underwriting, finance, TV and Radio Programming
- Developed external programming supported websites using HTML, ASP, SQL and JavaScript
- Shepherded the computer hardware resources and provided IT support for the company

SKILLS

Software	Hardware Platforms	Languages
Xilinx Suite	Spartan 3E FPGA	C(++)
Model Sim	Gumstix	Java
Pspice	Atmega328	HTML
Matlab	Philips LPC2131	SQL
Microsoft Office Suite	Ambric	VBScript
Adobe Suite	x86	Verilog
Macromedia Suite		VHDL

INTERESTS

Robotics, Strategy Games, Model Airplanes, Photography

PUBLICATIONS

B. Hutchings, B. Nelson, S. West, and R. Curtis. Optical Flow on the Ambric Massively Parallel Processor Array (MPPA). FCCM. April 2009

B. Hutchings, B. Nelson, S. West, and R. Curtis. Comparing Fine-Grained Performance on the Ambric MPPA against an FPGA. FPL. August 2009

REFERENCES | Sean Bond, Bluehost
sbond@bluehost.com

Dr. Clark Taylor, BYU Department of Electrical and Computer Engineering
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Dr. David Allred, BYU Department of Physics and Astronomy
(801) 422-3489
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Mark Mitchell, BYU Broadcasting
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